Memorandum

To   Lun-yi Lao

From: Kimberly Johnson

Date: 9/27/06

Re: Litigation search request us 6320574

Here are the results for Litigation no 6,320,574; this same search was performed in August so I just made a copy for you. Please feel free to contact me if you have additional questions or concerns. Thank you and have a great day

Kimberly Johnson
Technical Information Specialist
EIC 2600
2-4235

PALM INTRANET

Continuity Information for 10/720001

Parent Data
10720001
is a reissue of 09082070
Which is a continuation of 08803824

Child Data
11408528 is a division of 10720001
11408669 is a division of 10720001

| Appln Info | Contents | Petition Info | Atty/Agent Info | Continuity/Reexam | Foreign Data |

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Attorney Docket # [   ] Search

Bar Code # [   ] Search

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PALM INTRANET

Application Number Information

Application Number: **09/082070**
Assignments
Filing or 371(c) Date: **05/20/1998**
Effective Date: **05/20/1998**
Application Received: **05/20/1998**
Patent Number: **6320574**
Issue Date: **11/20/2001**
Date of Abandonment: **00/00/0000**
Attorney Docket Number: **PRDS-0010**
Status: **150 /PATENTED CASE**
Confirmation Number: **2273**

Examiner Number: **71067 / LAO, LUN-YI**

Group Art Unit: **2673**
Class/Subclass: **345/213.000**
Lost Case: **NO**
Interference Number:
Unmatched Petition: **NO**
L&R Code: Secrecy Code:**1**
Third Level Review: **NO**   Secrecy Order: **NO**
                                     Status Date: **11/01/2001**
Oral Hearing: **NO**

Title of Invention: **METHOD AND APPARATUS FOR CLOCK RECOVERY IN A DIGITAL DISPLAY UNIT**

Bar Code	PALM Location	Location Date	Charge to Loc	Charge to Name	Employee Name	Location
09082070	26M1	01/11/2006	26X1	LAO, LUN-YI	MESFIN, YIRGALEM	KNX/00/B 15

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Query/Command : prt max legalall

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1 / 1 PLUSPAT - ©QUESTEL-ORBIT

PN - 🈁 US6320574 B1 20011120 [US6320574]

TI - (B1) Circuit and method for generating pixel data elements from analog image data and associated synchronization signals

PA - (B1) GENESIS MICROCHIP CORP (US)

PA0 - Genesis Microchip, Corporation, Alviso CA [US]

IN - (B1) EGLIT ALEXANDER J (US)

AP - US8207098 19980520 [1998US-0082070]

FD - Cont. of US803824 19970224 [1997US-0803824]
Continuation of: US5796392

PR - US8207098 19980520 [1998US-0082070]
US80382497 19970224 [1997US-0803824]

IC - (B1) G09G-005/12

ICAA - G09G-003/20 [2006-01 A - I R M EP]; H03L-007/07 [2006-01 A - I R M EP]; H03L-007/085 [2006-01 A - I R M EP]; H03L-007/093 [2006-01 A - I R M EP]

ICCA - G09G-003/20 [2006 C - I R M EP]; H03L-007/07 [2006 C - I R M EP]; H03L-007/08 [2006 C - I R M EP]

EC - G09G-003/20
H03L-007/07
H03L-007/085
H03L-007/093

PCL - ORIGINAL (O) : 345213000; CROSS-REFERENCE (X) : 345501000
348512000 348537000

DT - Corresponding document

CT - US4018990; US4346407; US4488170; US4527145; US4554582; US4590602; US4616259; US4686560; US4694327; US4700217; US4703340; US4802009; US4893319; US5005079; US5010403; US5086295; US5101197; US5184091; US5185603; US5202669; US5280345; US5335018; US5359366; US5459766; US5497202; US5515108; US5528307; US5539473; US5574406; US5623311; EP0299724 A2; EP0405523 A2; EP0609843 A1; EP615222 A1; JP56-137736; JP63-14522; JP5-22277; JP7-334141; JP8-172353; JP8-340254; WO8905065

STG - (B1) U.S. Patent (no pre-grant pub.) after Jan. 2, 2001

AB - A clock recovery circuit in a digital display unit for recovering a time reference signal associated with analog display data. The clock recovery circuit includes a phase-locked loop (PLL) implemented in digital domain and an analog filter to eliminate any undesirable frequencies from the output signal of the PLL. The PLL includes independent control loops to track long term frequency drifts of the time reference signal and the transient phase differences respectively. By providing such independent control loops, the generated clock can be better synchronized with the time reference signal.

UP - 2001-48

PN   -   US6320574 B1 20011120 [US6320574]
AP   -   US8207098 19980520 [1998US-0082070]
ACT   -   20041019 US/RF-A
                 REISSUE APPLICATION FILED
                 EFFECTIVE DATE: 20031120
UP   -   2004-46

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1 / 1   CRXX - ©CLAIMS/RRX

PN   -   6,320,574 A 20011120 [US6320574]
PA   -   Genesis Microchip Corp
ACT   -   20031120 REISSUE REQUESTED
                 ISSUE DATE OF O.G.: 20041019
                 REISSUE REQUEST NUMBER: 10/720001
                 EXAMINATION GROUP RESPONSIBLE FOR REISSUEPROCESS: 2673

                 Reissue Patent Number:

Search statement   4

UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT

6320574

November 20, 2001

Circuit and method for generating pixel data elements from
analog image data and associated synchronization signals

REISSUE: November 20, 2003 - Reissue Application filed Ex. Gp.: 2673; Re. S.N.
10/720,001 (O.G. October 19, 2004)

APPL-NO: 082070 (09)

FILED-DATE: May 20, 1998

GRANTED-DATE: November 20, 2001

CORE TERMS: display, frequency, clock, analog, digital, phase, correction,
pixel, loop, screen ...

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6,320,574 OR 6320574

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Patent Search 6320574 8/7/2006

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